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APPLICANTS

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** CONTINUING DATA *****

** FOREIGN APPLICATIONS *****

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 07/30/2004

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY TX	SHEETS DRAWING 5	TOTAL CLAIMS 22	INDEPENDENT CLAIMS 4
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance <i>72</i>				
Verified and Acknowledged	Examiner's Signature <i>72</i>	Initials <i>72</i>		

ADDRESS

23494
 TEXAS INSTRUMENTS INCORPORATED
 P O BOX 655474, M/S 3999
 DALLAS , TX
 75265

TITLE

Low resistance integrated circuit MOS transistor *with reduced drain and source resistance*

*(from non-patent
 filed 3/1/05)*

FILING FEE RECEIVED 870	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees
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